WHAT IS CLAIMED IS:

1   1. A synchronization control method for an electronic device connectable to a

2 serial interface, said synchronization control method comprising:

3   said electronic device extracting a clock component from information flowing on

4 said serial interface even when said electronic device is not receiving data; and

5   said electronic device monitoring a synchronous state of a data reception timing

6 clock generated by said electronic device itself with respect to said extracted clock component

7 (clock).

1   2. The synchronization control method as claimed in claim 1, further comprising:

2   when said electronic device has detected that a clock made up of said clock

3 component extracted from said information flowing on said serial interface is out of

4 synchronization with said data reception timing clock generated by said electronic device itself,

5 said electronic device performing self-synchronization operation such that said timing clock

6 generated by said electronic device itself synchronizes with said clock made up of said extracted

7 clock component.

1   3. An electronic device comprising:

2   a serial interface;

3   circuitry configured to generate a timing clock; and

4   synchronization control circuitry configured to

5   extract a clock component from information present on said serial

6 interface, even when said information is not addressed to said electronic device, and

7   monitor a synchronous state of said timing clock generated by said

8 electronic device itself with respect to said extracted clock component.

1   4. The electronic device of claim 1 wherein said synchronization control circuitry

2 is further configured to synchronize said timing clock with said extracted clock component in

3 response to determining that said timing clock is out of synchronization with said extracted clock

4 component.

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